

Applicant respectfully requests that Figure 2 be renamed Figure 1, and amended to correct descriptive legends in blocks 11 - 13, respectively, pursuant to 37 C.F.R. 1.84(o), as indicated in Exhibit A, attached hereto.

Applicant respectfully requests that Figure 3 be renamed Figure 2, and amended to correct descriptive legends in block 22 and block 23, respectively, pursuant to 37 C.F.R. 1.84(o), as indicated in Exhibit A, attached hereto.

Applicant respectfully requests that Figure 4 be renamed Figure 3, and amended to correct descriptive legends in block 32 and block 33, respectively, pursuant to 37 C.F.R. 1.84(o), as indicated in Exhibit A, attached hereto.

Applicant respectfully requests that Figure 5 be renamed Figure 4, and amended as indicated in Exhibit A, attached hereto.

Also attached hereto is Exhibit B which contains newly amended Figures 1 – 4 for review by the Examiner. Applicant respectfully requests approval of the proposed amendments to original Figures 1 – 5.

**IN THE CLAIMS:**

Please amend claims 1-21, inclusive, as follows:

1(Amended). A cryptographic device adapted to perform data encryption and decryption on at least one data stream flowing between at least one data generating device and at least one data storage device without compromising overall system performance.

A24 2(Amended). A cryptographic device adapted to perform data encryption on at least one data stream flowing between at least one data generating device and at least one data storage device without compromising overall system performance.

3(Amended). A cryptographic device adapted to perform data decryption on at least one data stream flowing between at least one data generating device and at least one data storage device without compromising overall system performance.

4(Amended). A cryptographic device adapted to intercept at least one data stream flowing between at least one data generating device and at least one data storage device, and transparently perform data encryption in accordance with said at least one intercepted data stream.

5(Amended). A cryptographic device adapted to intercept at least one data stream flowing between at least one data generating device and at least one data storage device, and transparently perform data decryption in accordance with said at least one intercepted data stream.

A24 6(Amended). A cryptographic device adapted to intercept at least one data stream flowing between at least one data generating device and at least one data storage device, and transparently perform data encryption and decryption in accordance with said at least one intercepted data stream.

7(Amended). A cryptographic device, comprising:

at least one data stream interceptor;

a main controller receiving input from said at least one data stream interceptor;

at least one data generating controller adapted to perform at least one data transfer protocol with at least one data generating device on command from said main controller;

at least one data storage controller adapted to perform at least one data transfer protocol with at least one data storage device on command from said main controller; and

at least one cipher engine adapted to transparently encrypt at least one data stream flowing between said at least one data generating device and said at least one data storage device on command from said main controller.

8(Amended). The cryptographic device of claim 7, wherein said at least one cipher engine is operatively coupled between at least one input buffer and at least one output buffer.

9(Amended). The cryptographic device of claim 8, wherein said at least one input buffer receives data from said at least one data generating device and said at least one data storage device.

10(Amended). The cryptographic device of claim 8, wherein said at least one output buffer outputs data to said at least one data generating device and said at least one data storage device.

11(Amended). A cryptographic device, comprising:

at least one data stream interceptor;

a main controller receiving input from said at least one data stream interceptor;

A24 at least one data generating controller adapted to perform at least one data transfer protocol with at least one data generating device on command from said main controller;

at least one data storage controller adapted to perform at least one data transfer protocol with at least one data storage device on command from said main controller; and

at least one cipher engine adapted to transparently decrypt at least one data stream flowing between said at least one data generating device and said at least one data storage device on command from said main controller.

12(Amended). The cryptographic device of claim 11, wherein said at least one cipher engine is operatively coupled between at least one input buffer and at least one output buffer.

13(Amended). The cryptographic device of claim 12, wherein said at least one input buffer receives data input from said at least one data generating device and said at least one data storage device.

14(Amended). The cryptographic device of claim 12, wherein said at least one output buffer outputs data to said at least one data generating device and said at least one data storage device.

15(Amended). A cryptographic device, comprising:

at least one data stream interceptor;

a main controller receiving input from said at least one data stream interceptor;

at least one data generating controller adapted to perform at least one data transfer protocol with at least one data generating device on command from said main controller;

at least one data storage controller adapted to perform at least one data transfer protocol with at least one data storage device on command from said main controller; and

at least one cipher engine adapted to transparently encrypt and decrypt at least one data stream flowing between said at least one data generating device and said at least one data storage device on command from said main controller.

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16(Amended). The cryptographic device of claim 15, wherein said at least one cipher engine is operatively coupled between at least one input buffer and at least one output buffer.

17(Amended). The cryptographic device of claim 16, wherein said at least one input buffer receives data from said at least one data generating device and said at least one data storage device.

18(Amended). The cryptographic device of claim 16, wherein said at least one output buffer outputs data to said at least one data generating device and said at least one data storage device.

19(Amended). A cryptographic device operatively coupled between a data generating device and a data storage device for use during data transfer, said cryptographic device comprising:

a data stream interceptor;

a main controller receiving input from said at least one data stream interceptor;

a data generating controller adapted to perform at least one data transfer protocol with the data generating device on command from said main controller;

a data storage controller adapted to perform at least one data transfer protocol with the data storage device on command from said main controller; and

a cipher engine adapted to transparently encrypt and decrypt at least one data stream flowing between the data generating device and the data storage device on command from said main controller.

20(Amended). A cryptographic device integrated within a data storage device for use during data transfer with a data generating device, said cryptographic device comprising:

a data stream interceptor;

A24 a main controller receiving input from said data stream interceptor;

a data generating controller adapted to perform at least one data transfer protocol with the data generating device on command from said main controller;

a data storage controller adapted to perform at least one data transfer protocol with the data storage device on command from said main controller; and

a cipher engine adapted to transparently encrypt and decrypt at least one data stream flowing between the data generating device and the data storage device on command from said main controller.

21(Amended). A cryptographic device integrated within a data generating device for use during data transfer with a data storage device, said cryptographic device comprising:

a data stream interceptor;

a main controller receiving input from said data stream interceptor;

a data generating controller adapted to perform at least one data transfer protocol with the data generating device on command from said main controller;

a data storage controller adapted to perform at least one data transfer protocol with the data storage device on command from said main controller; and